

BODELAIRE: A TPC for Neutron Science

DPG Spring Meeting Karlsruhe 2024

08.03.24 - T 116.1

Thomas Block¹, Klaus Desch¹, Jochen Kaminski¹, Saime Gürbuz¹, Michael Lupberger^{1,2}, Markus Köhli³, Jan Glowacz¹, Markus Gruber¹, Laura Rodriguez Gómez¹

¹Physikalisches Institut, Universität Bonn

²HISKP, Universität Bonn

³Physikalisches Institut, Universität Heidelberg



Neutron science



Using thermal neutron beams as a non-evasive probe

Neutron science



Using thermal neutron beams as a non-invasive probe

Study atomic and magnetic structure and dynamics of condensed matter

Neutron science



Using thermal neutron beams as a non-invasive probes

Study atomic and magnetic structure and dynamics of condensed matter

Advantages over X-rays: Cross section for thermal neutrons irregular

Neutron science



Using thermal neutron beams as a non-invasive probes

Study atomic and magnetic structure and dynamics of condensed matter

Advantages over X-rays: Cross section for thermal neutrons irregular

→ Higher contrast between isotopes and elements with similar Z

Neutron science - Example: Imaging

RS-232 connector

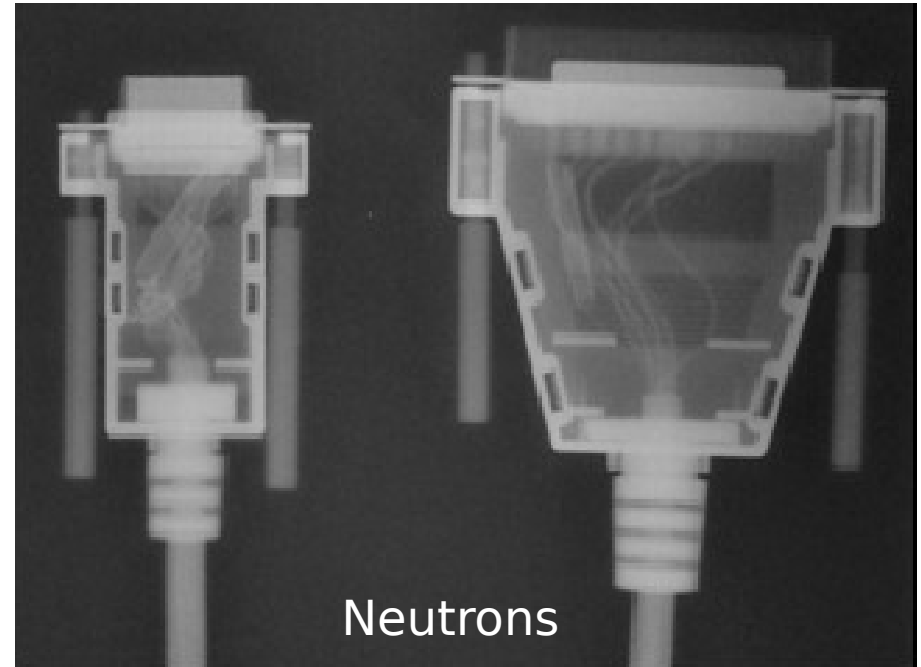
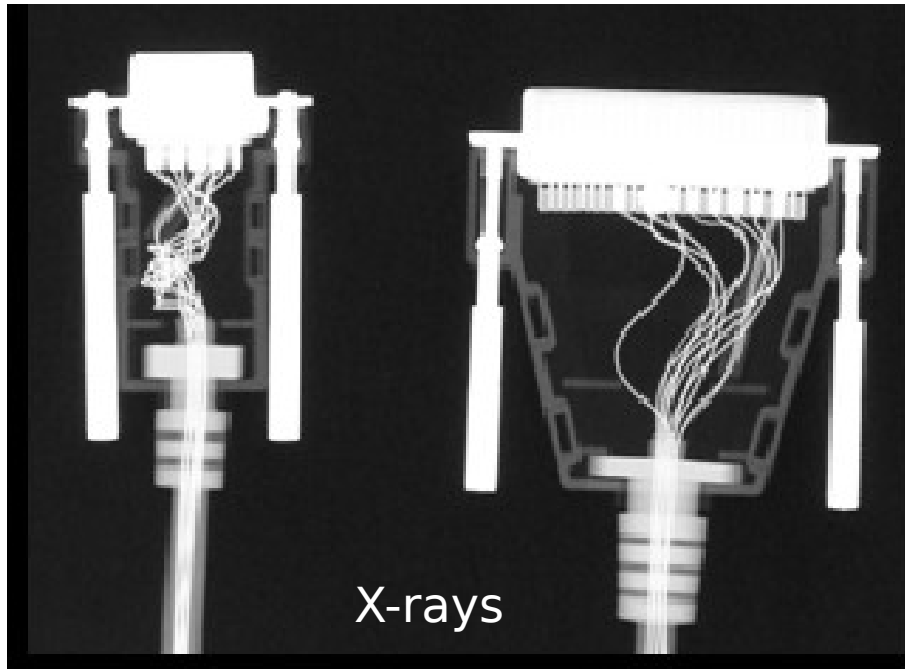
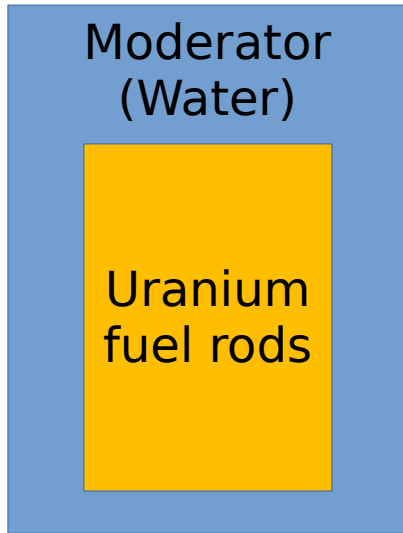


Image source: [DOI:10.5772/35650](https://doi.org/10.5772/35650)

Neutron science - Basic setup

Neutron source



Guide



Sample



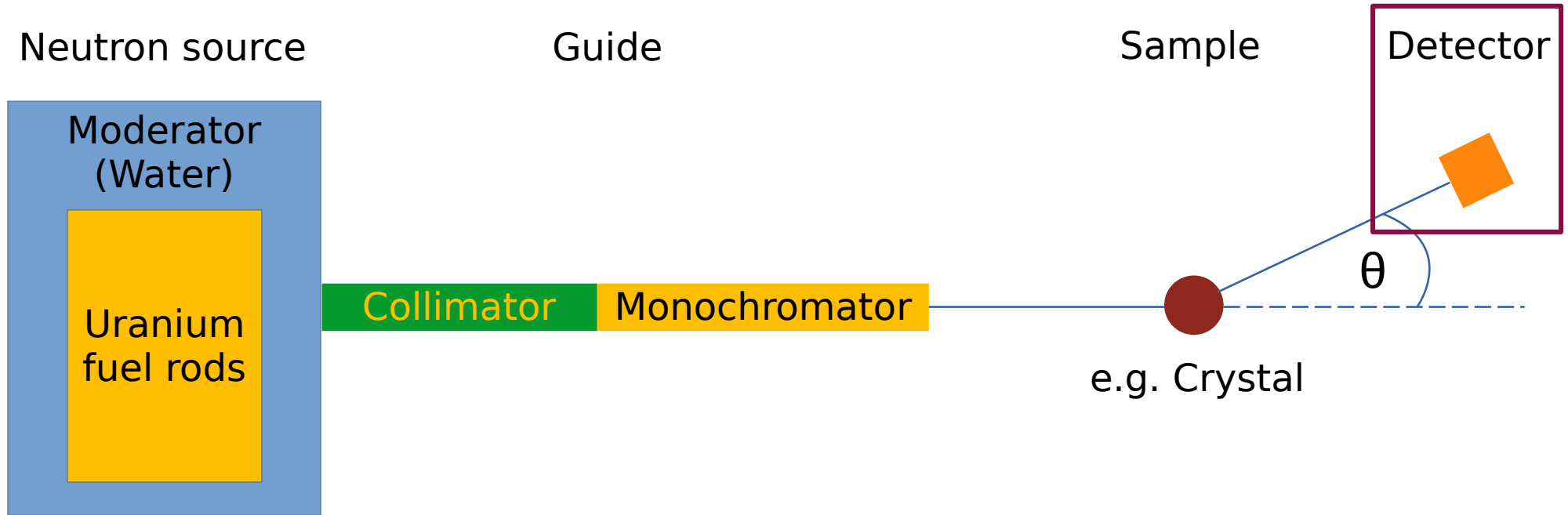
e.g. Crystal

Detector



θ

Neutron science - Basic setup



Neutron science - What is the problem?



^3He heavily used as a neutron converter

Neutron science - What is the problem?



^3He heavily used as a neutron converter

But: Increasing demand, i.e. **exploding costs**, of He-3

Neutron science - What is the problem?



^3He heavily used as a neutron converter

But: Increasing demand, i.e. **exploding costs**, of He-3

Need an **alternative**: We want **solid converters** with

Neutron science - What is the problem?



^3He heavily used as a neutron converter

But: Increasing demand, i.e. **exploding costs**, of He-3

Need an **alternative**: We want **solid converters** with

- **high** absorption cross section for thermal neutrons

Neutron science - What is the problem?



^3He heavily used as a neutron converter

But: Increasing demand, i.e. **exploding costs**, of He-3

Need an **alternative**: We want **solid converters** with

- **high** absorption cross section for thermal neutrons
- **easy** to handle and process

Neutron science - What is the problem?



^3He heavily used as a neutron converter

But: Increasing demand, i.e. **exploding costs**, of He-3

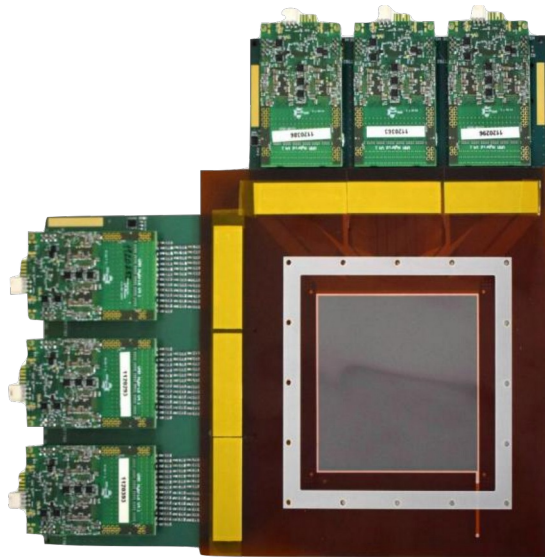
Need an **alternative**: We want **solid converters** with

- **high** absorption cross section for thermal neutrons
- **easy** to handle and process

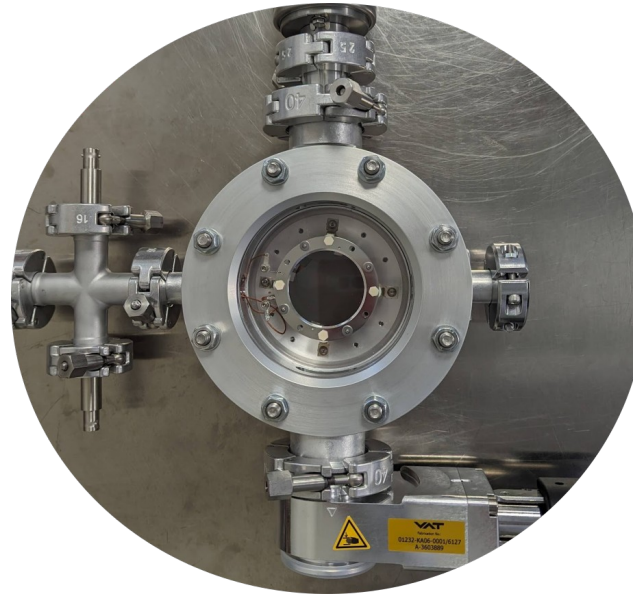
^{10}B is our choice!

Neutron detectors in GasDet group

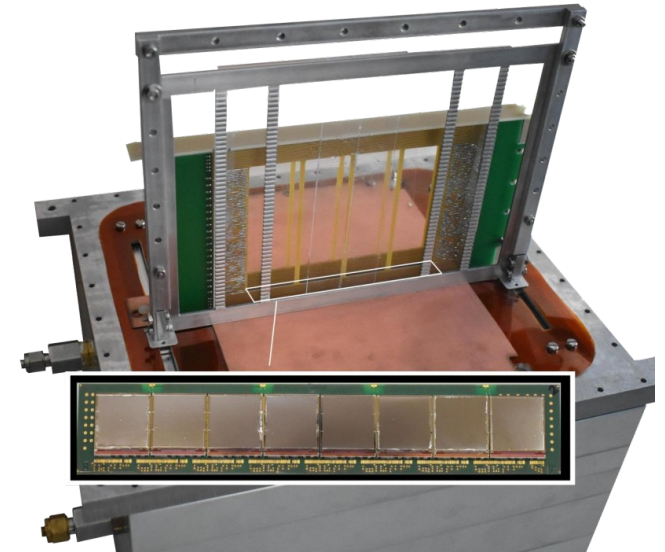
Boron lined
GEM-VMM Hybrid



Neutron sensitive
Microchannel Plate

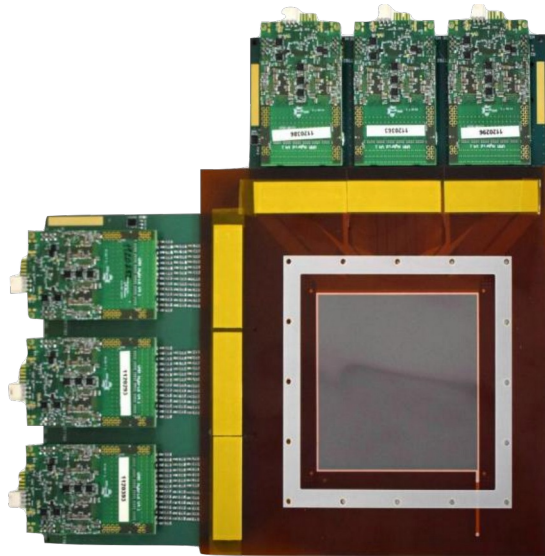


Neutron Time
Projection Chamber

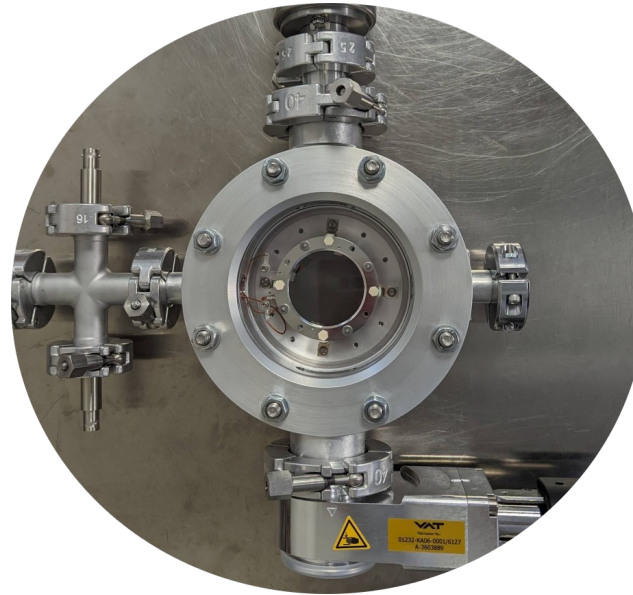


Neutron detectors in GasDet group

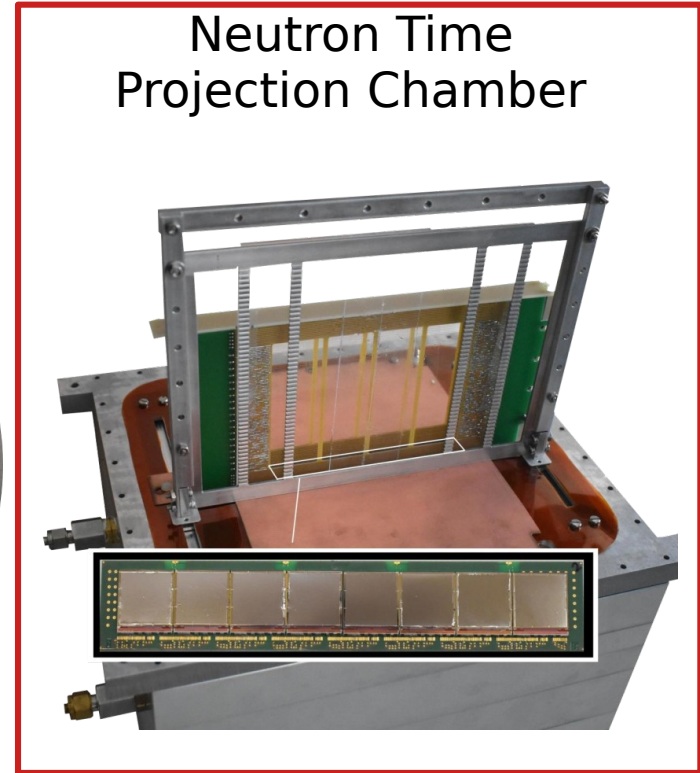
Boron lined
GEM-VMM Hybrid



Neutron sensitive
Microchannel Plate



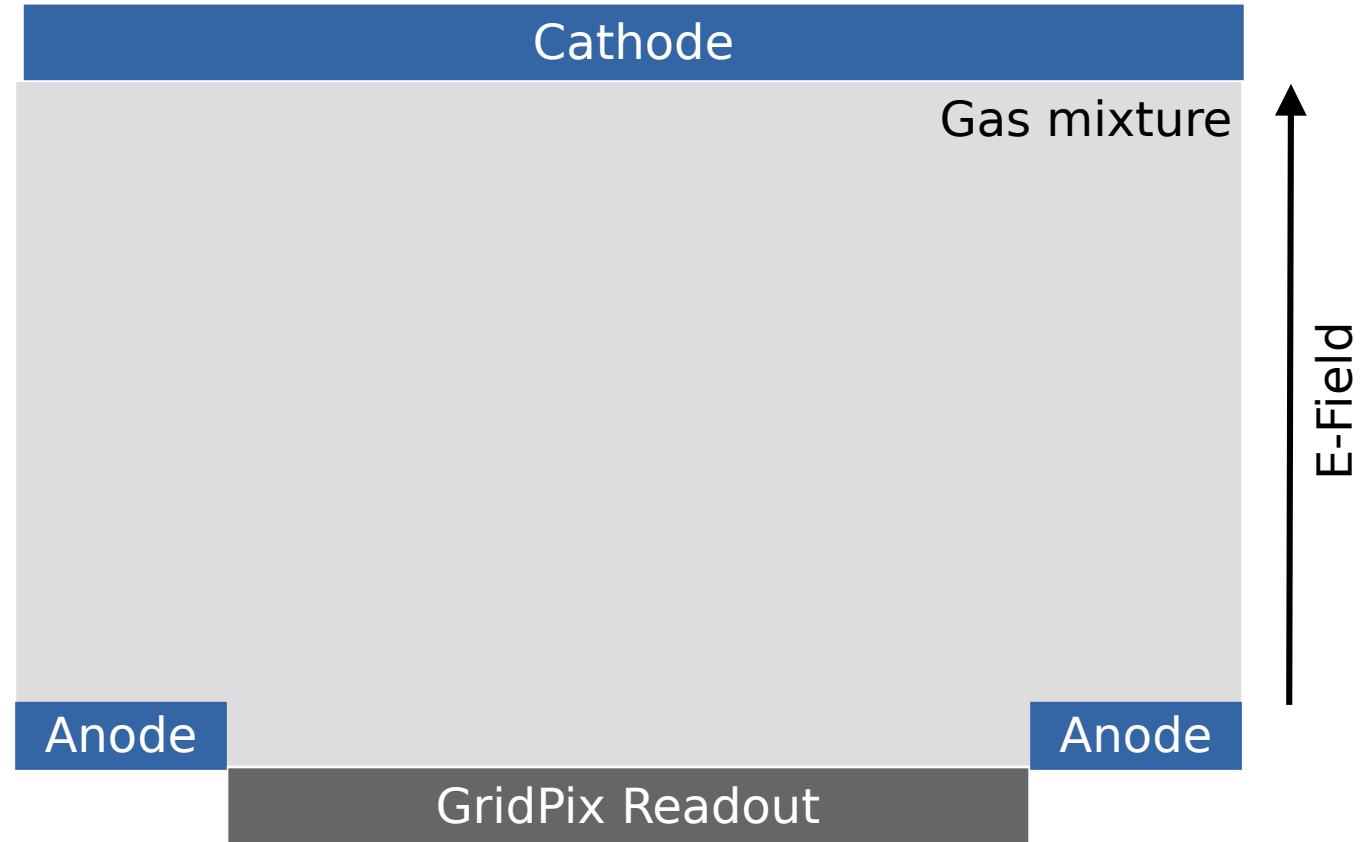
Neutron Time
Projection Chamber



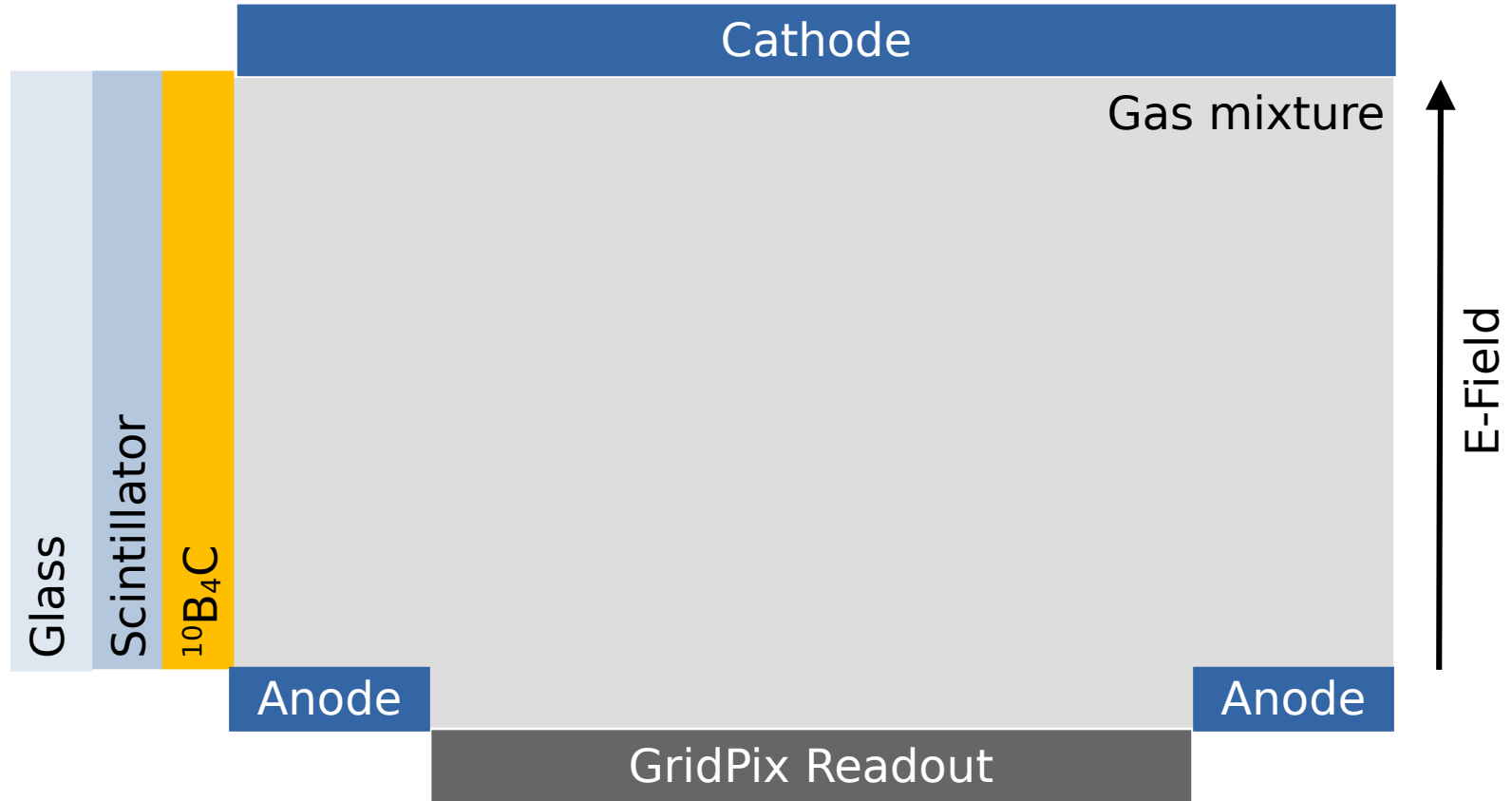
Detector concept - Neutron TPC



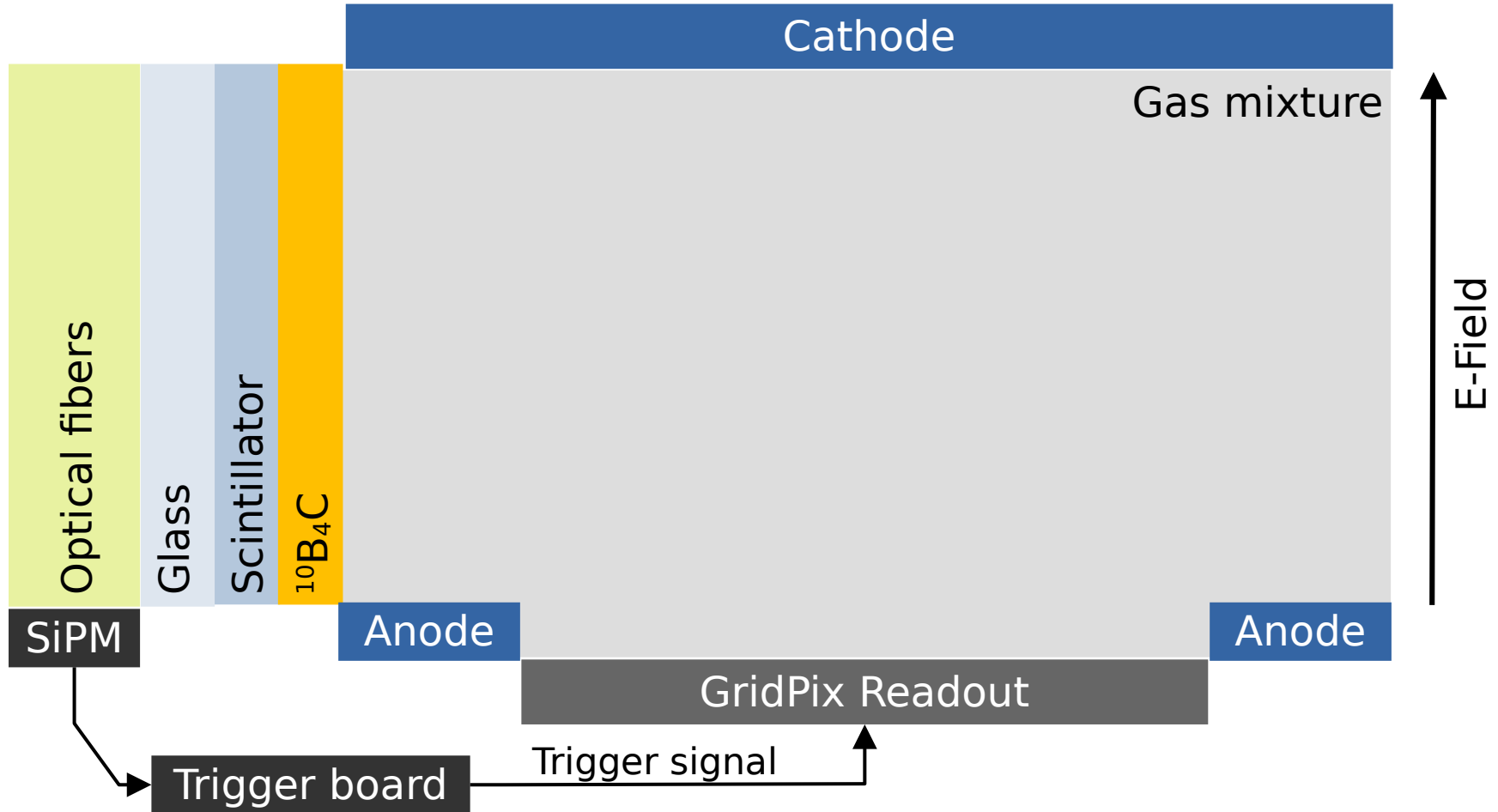
Detector concept - Neutron TPC



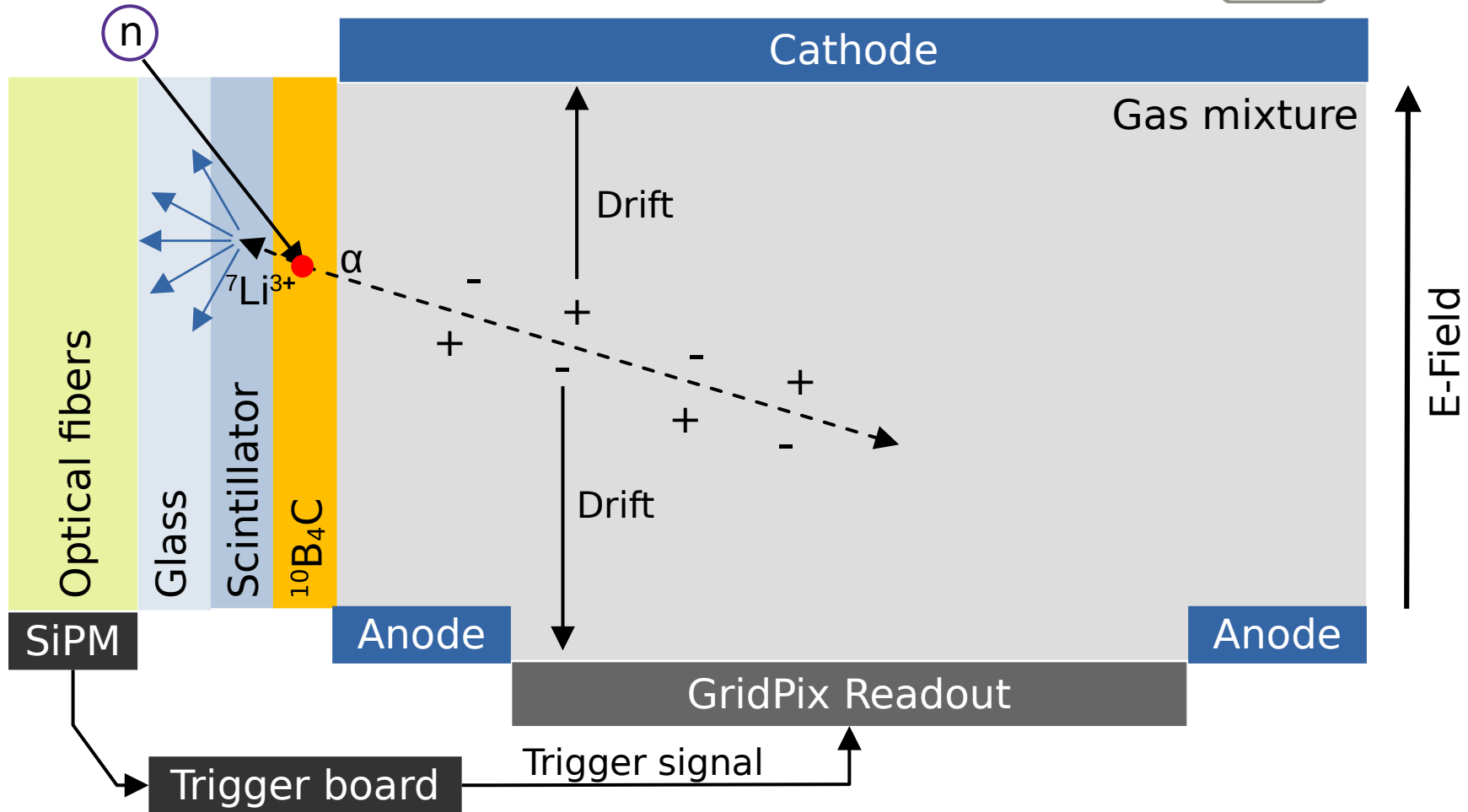
Detector concept - Neutron TPC



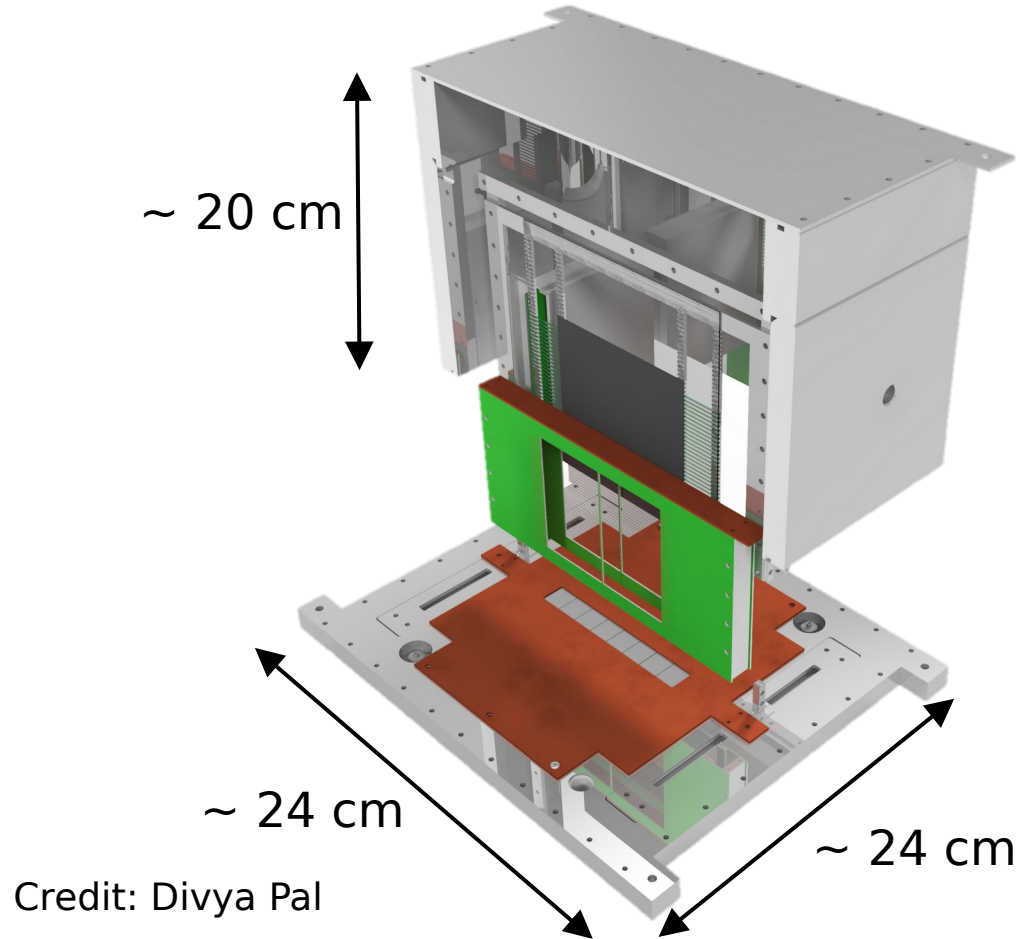
Detector concept - Neutron TPC



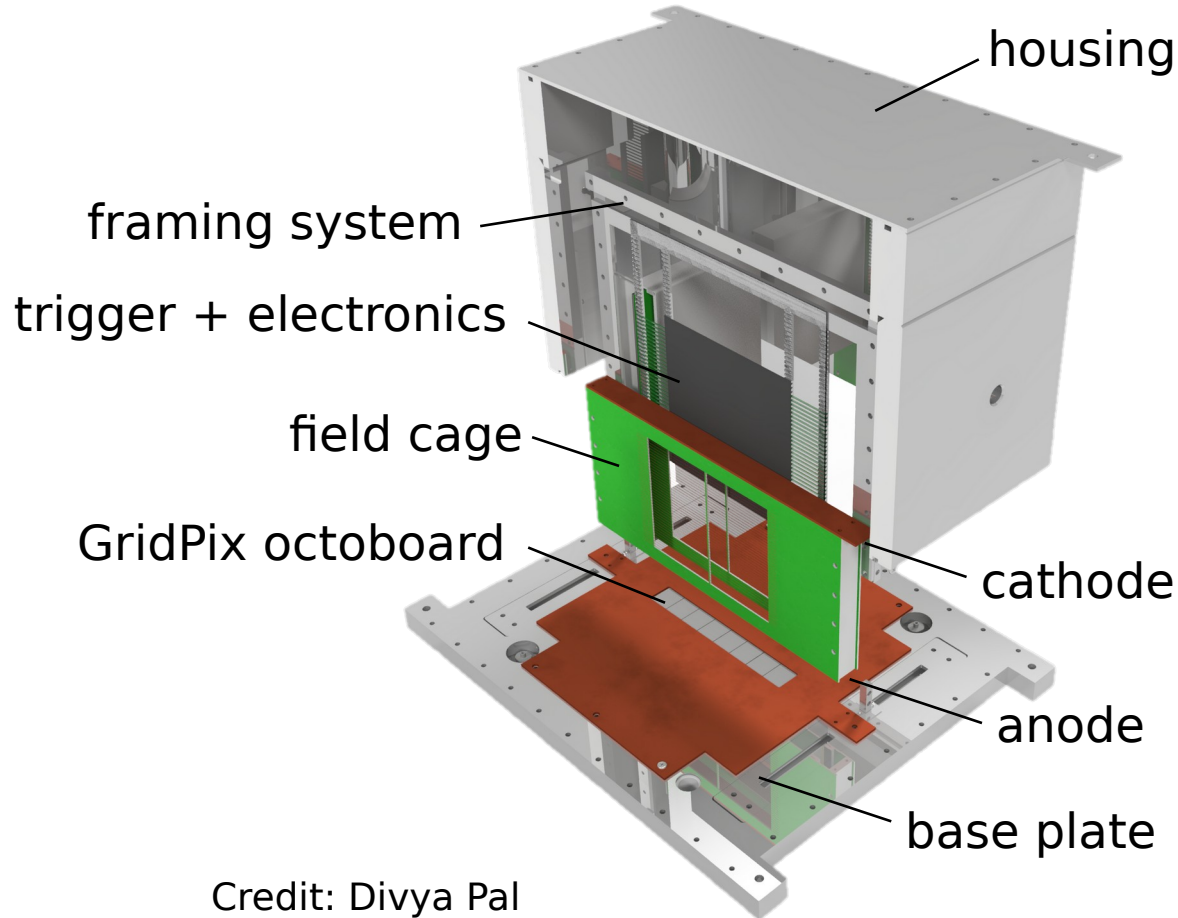
Detector concept - Neutron TPC



Current detector design

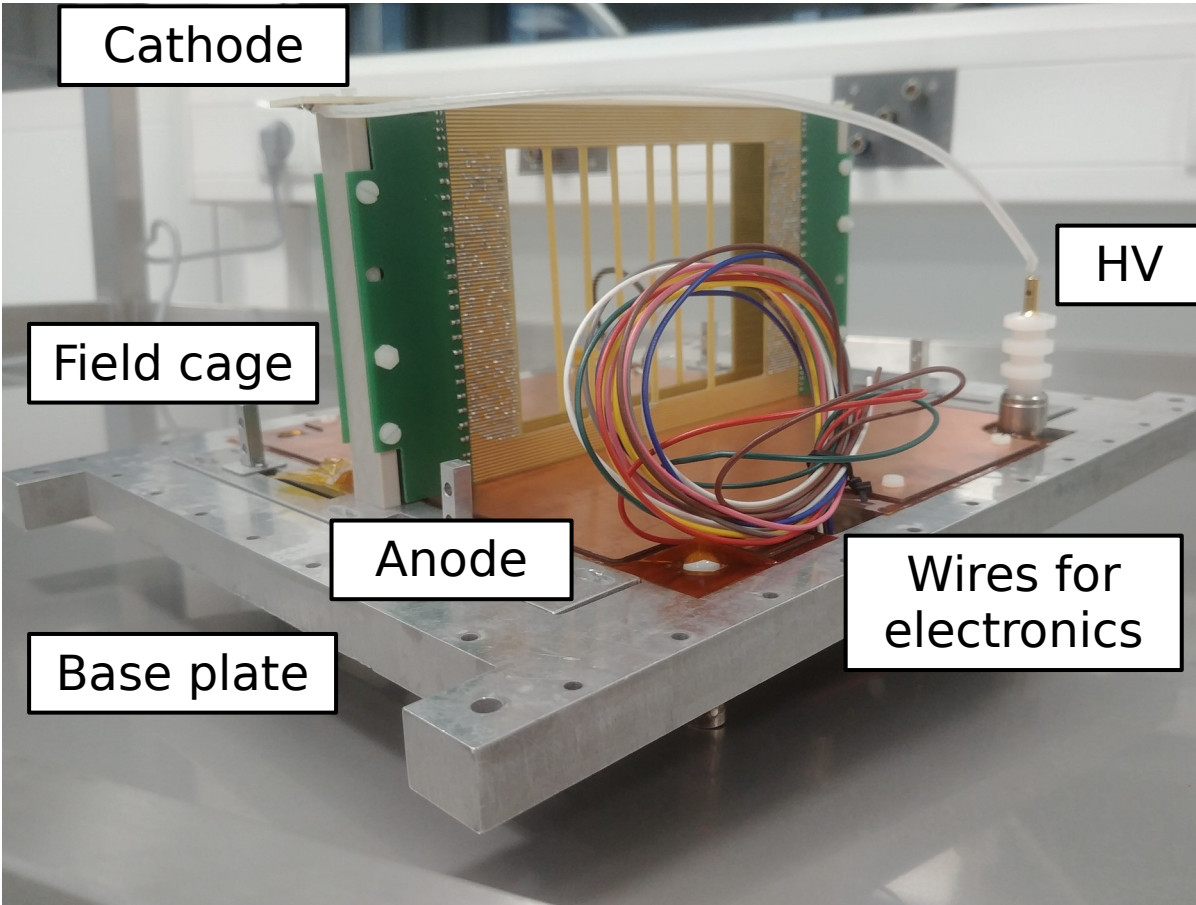


Current detector design

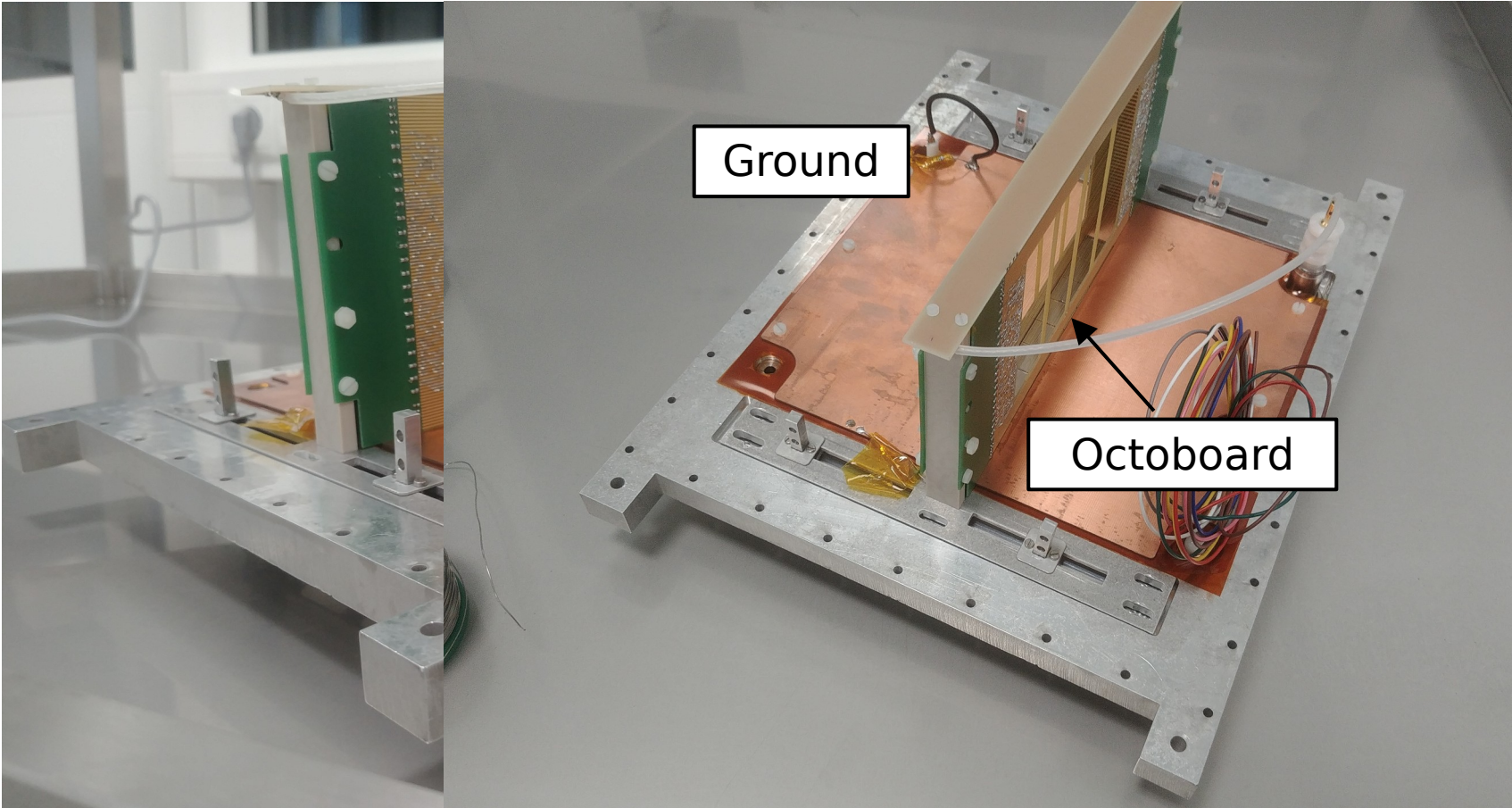


Credit: Divya Pal

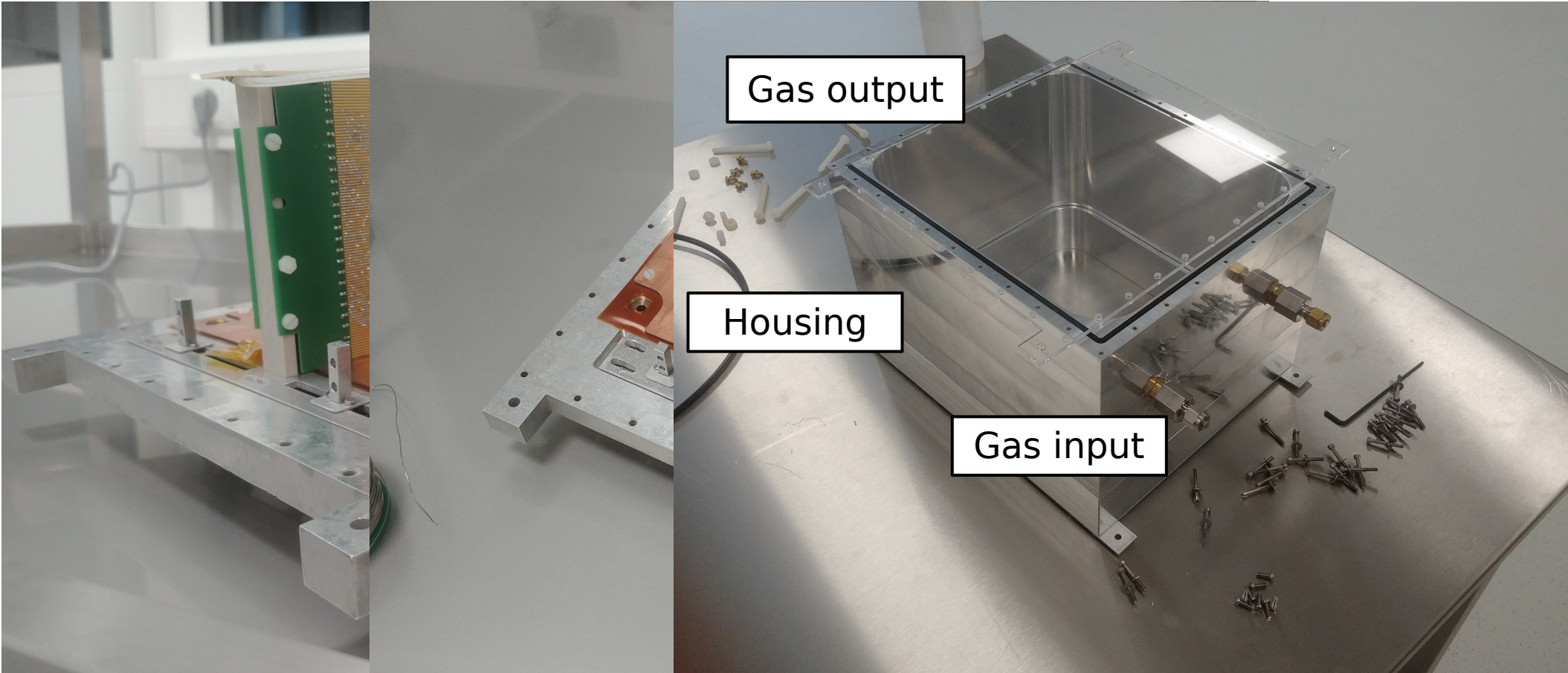
Current detector design



Current detector design



Current detector design



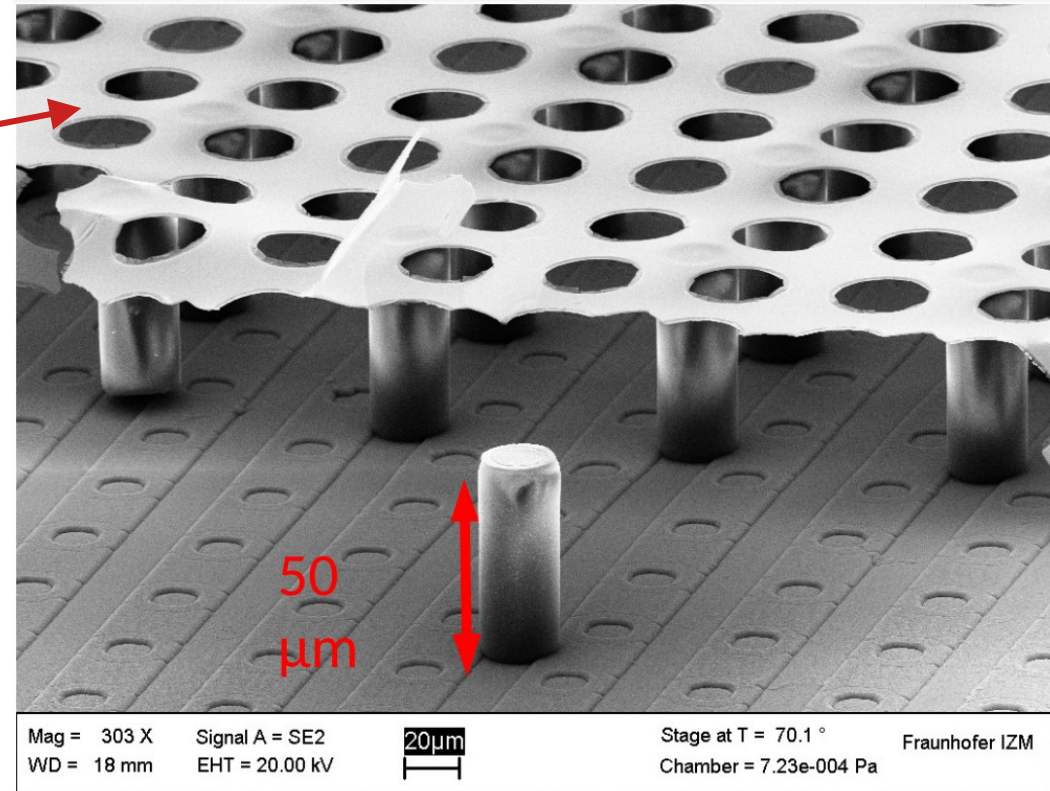
Detector concept - GridPix chip



GridPix =
Timepix ASIC +
Integrated Micromegas grid
(InGrid)

Detector concept - GridPix chip

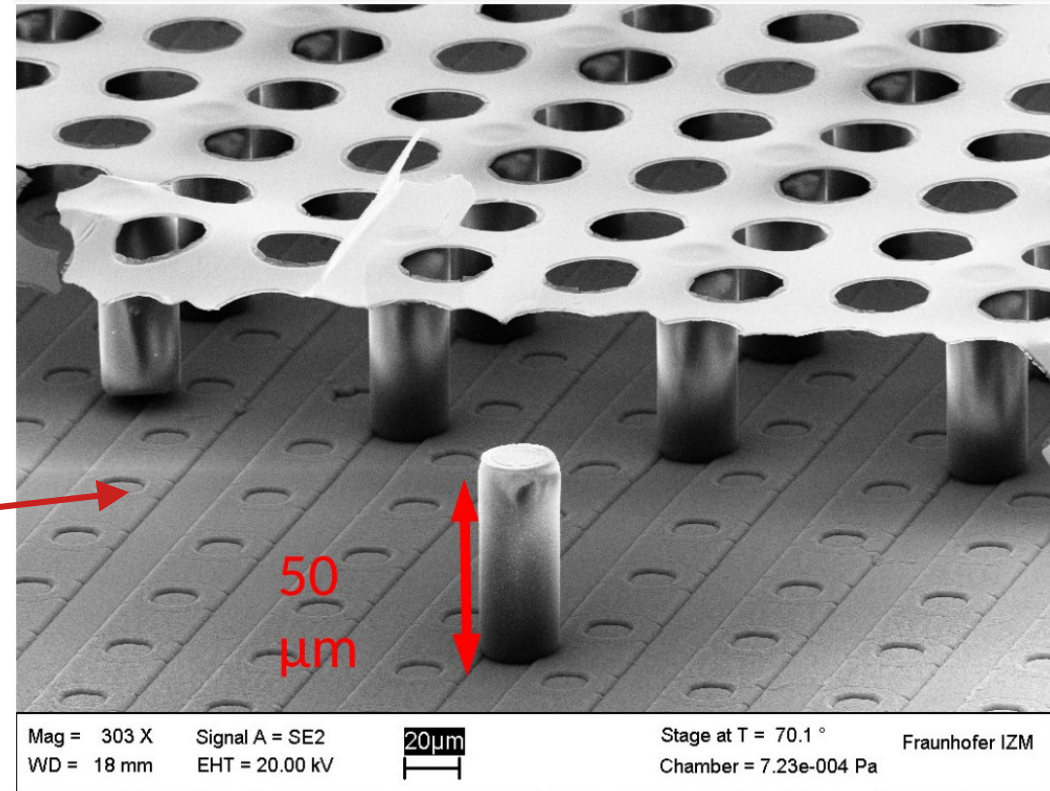
GridPix =
Timepix ASIC +
Integrated Micromegas grid
(InGrid)



Detector concept - GridPix chip

GridPix =
Timepix ASIC +
Integrated Micromegas grid
(InGrid)

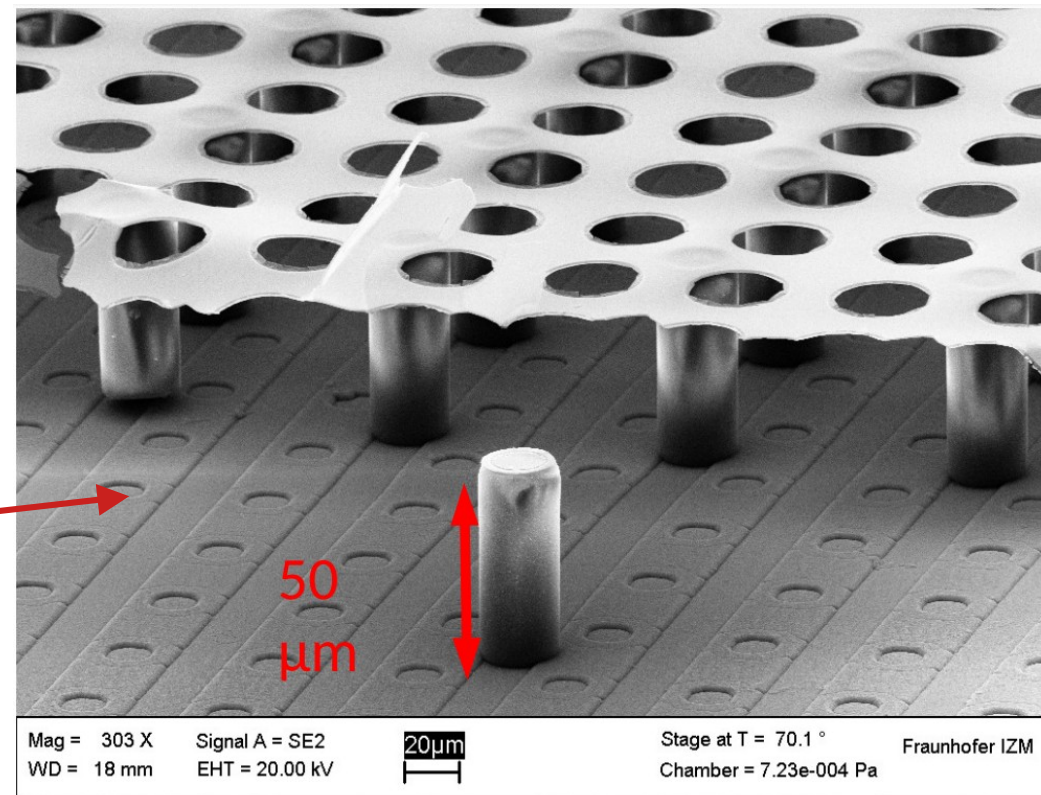
256 x 256 pixels
Pitch: 55 μm
ToT and ToA
Clock frequency: 40/80 MHz



Detector concept - GridPix chip

GridPix =
Timepix ASIC +
Integrated Micromegas grid
(InGrid)

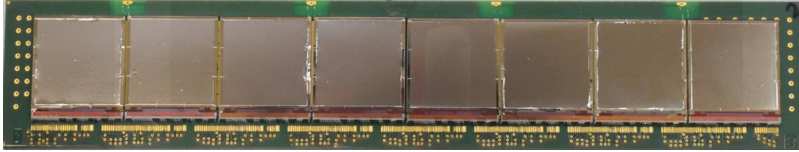
256 x 256 pixels
Pitch: 55 μm
ToT and ToA
Clock frequency: 40/80 MHz



How to produce: Sabine Hartung (T 93.3)

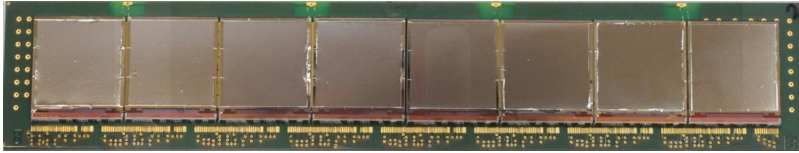
Detector concept - GridPix readout chain

GridPix Octoboard

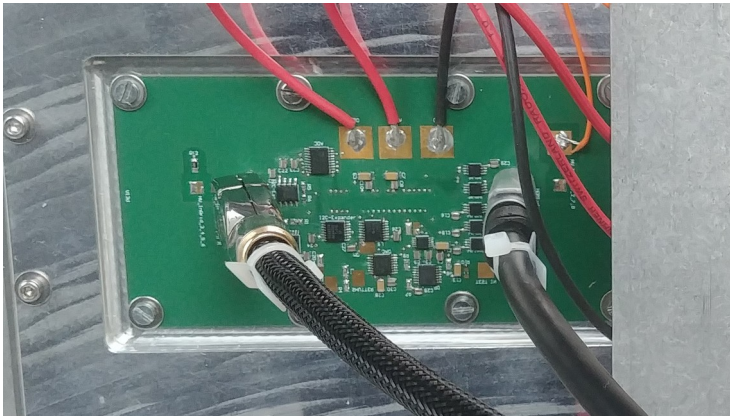


Detector concept - GridPix readout chain

GridPix Octoboard

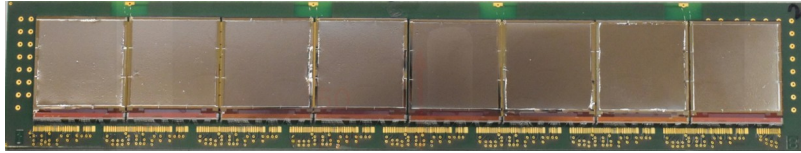


Intermediate board

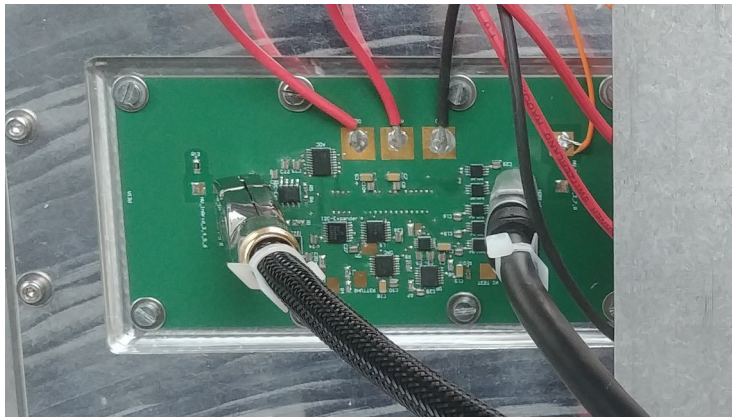


Detector concept - GridPix readout chain

GridPix Octoboard



Intermediate board

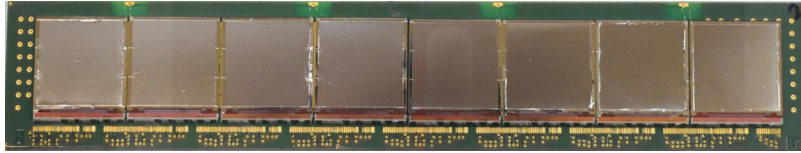


Adapter and concentrator card

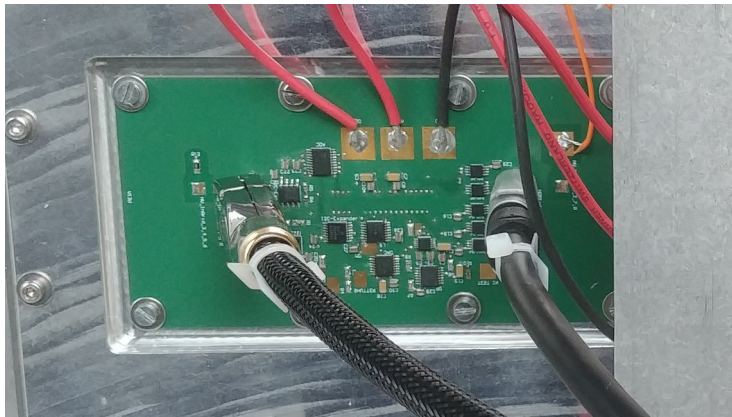


Detector concept - GridPix readout chain

GridPix Octoboard



Intermediate board



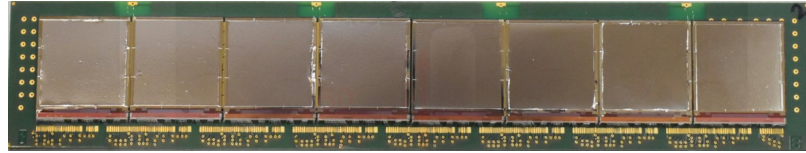
Adapter and concentrator card



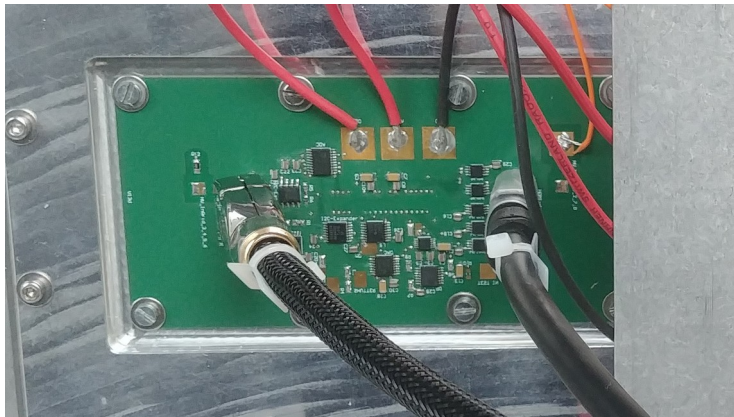
PC

Detector concept - GridPix readout chain

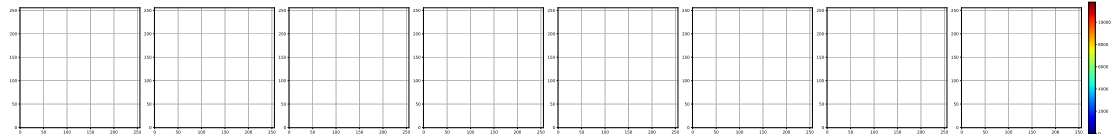
GridPix Octoboard



Intermediate board



PC



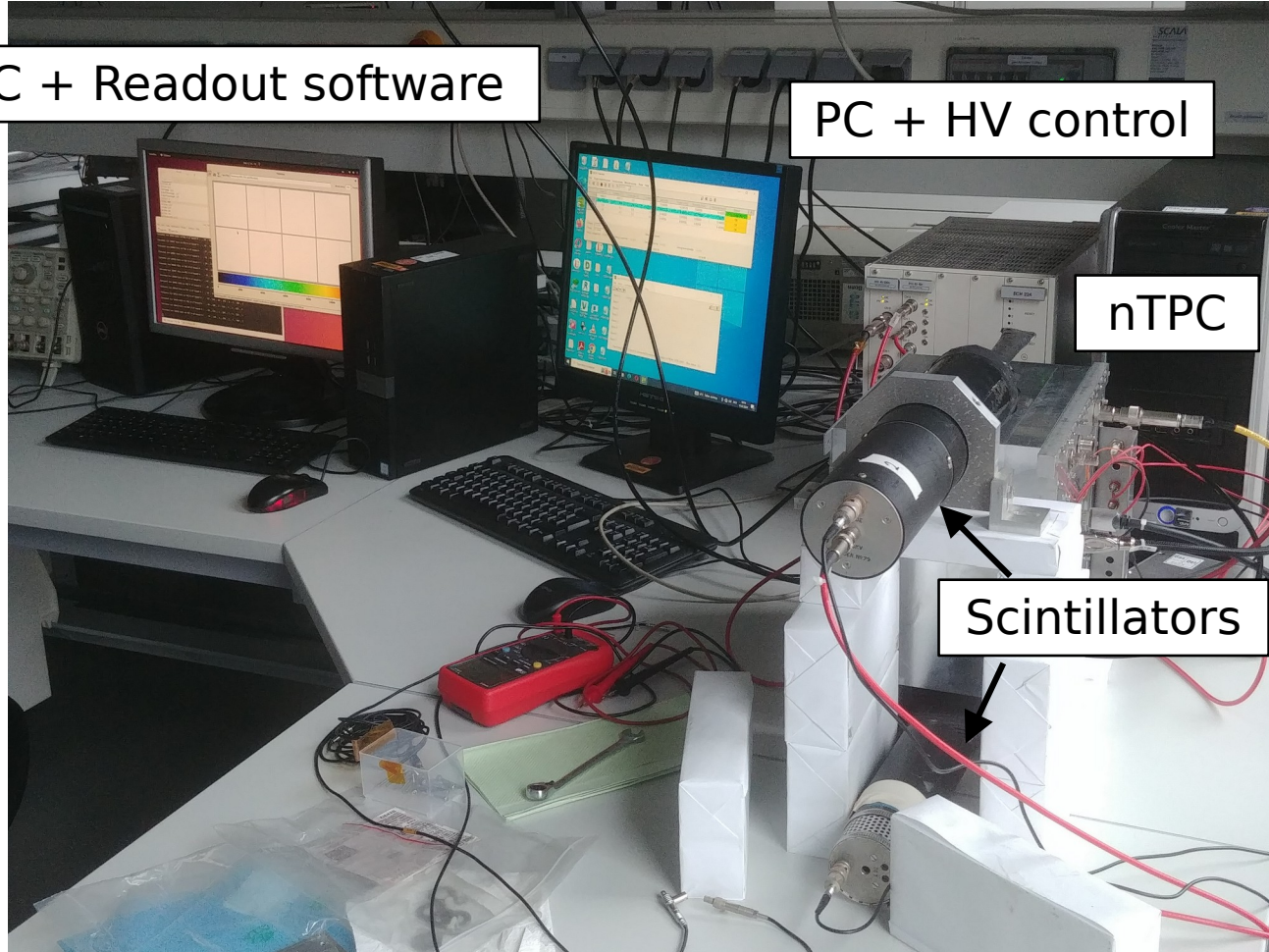
Current setup

PC + Readout software

PC + HV control

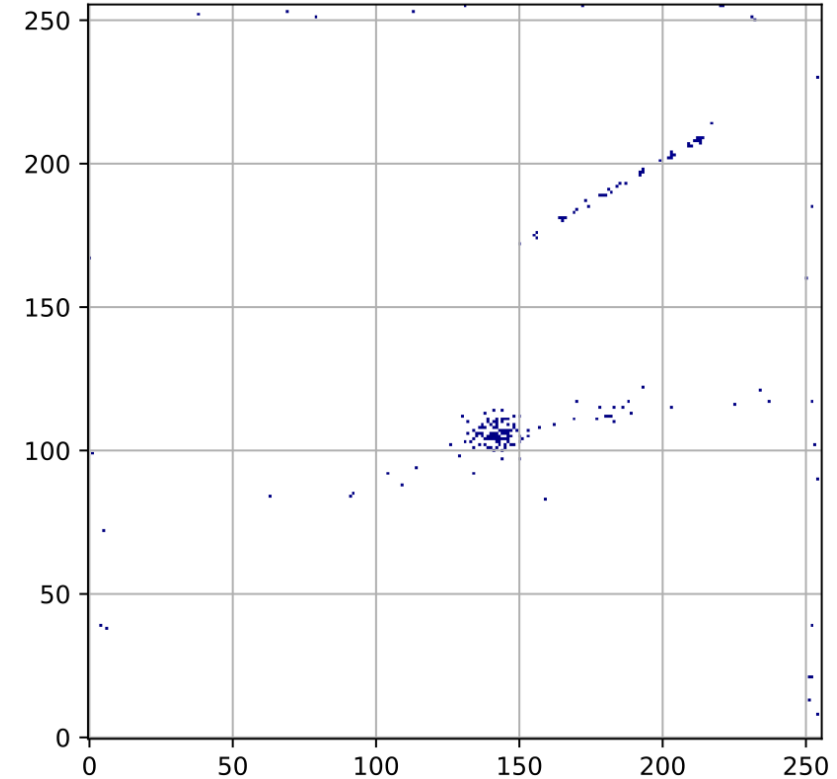
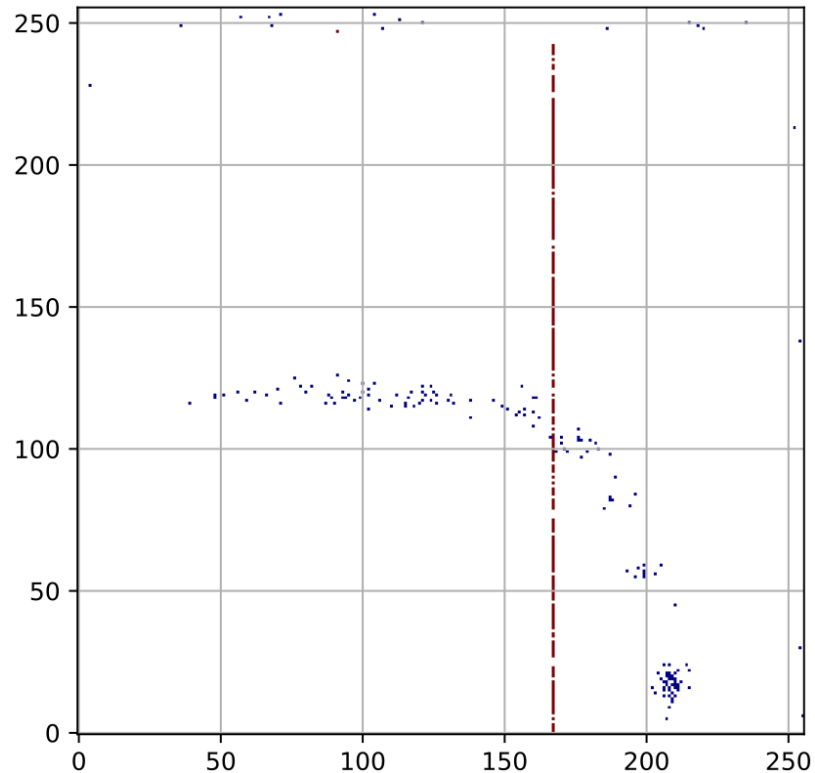
nTPC

Scintillators



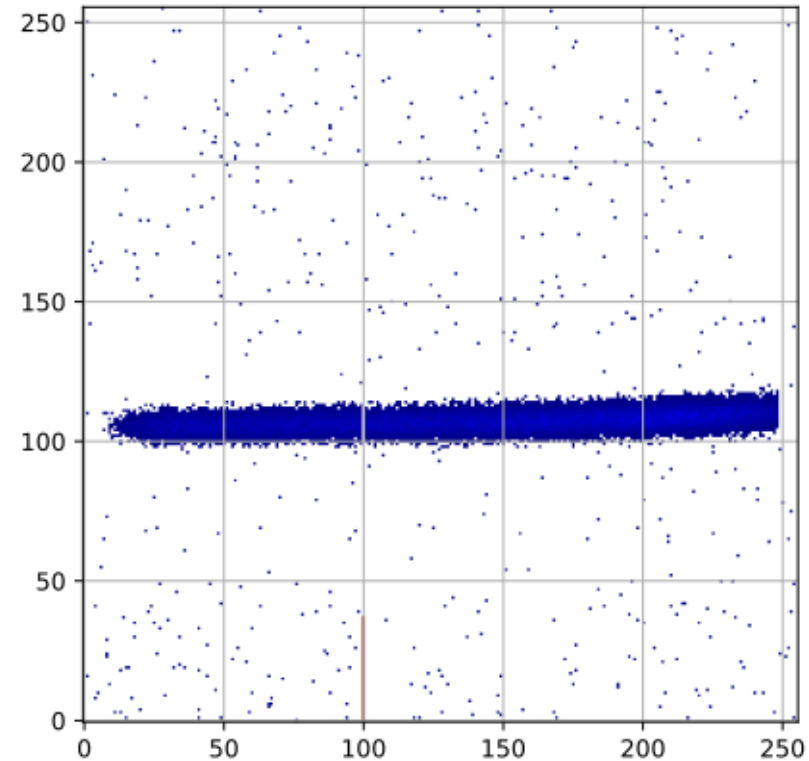
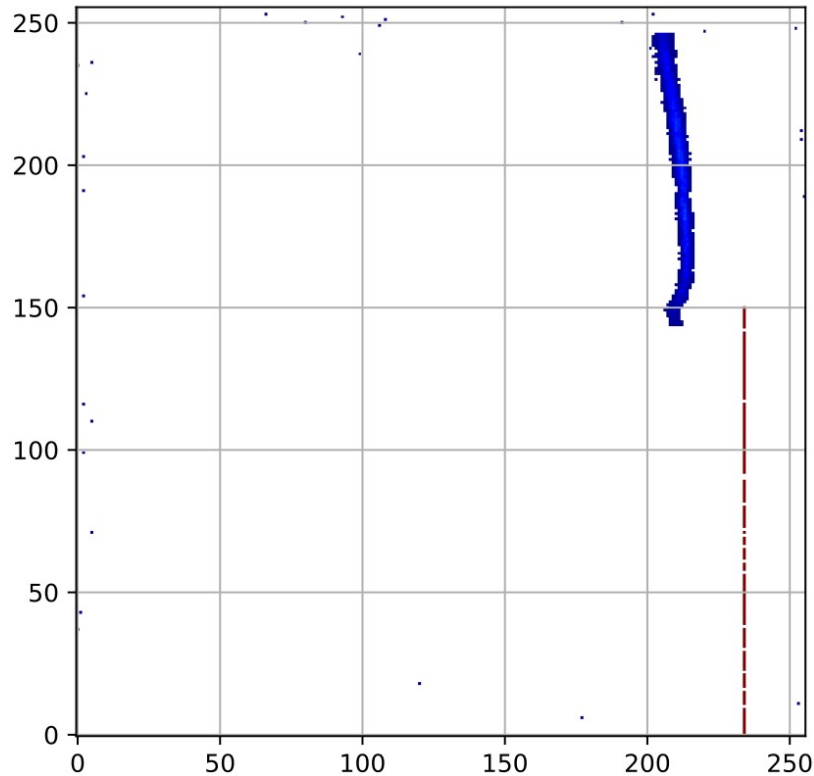
Untriggered events - signatures

Lightly ionizing particles + Bragg peak

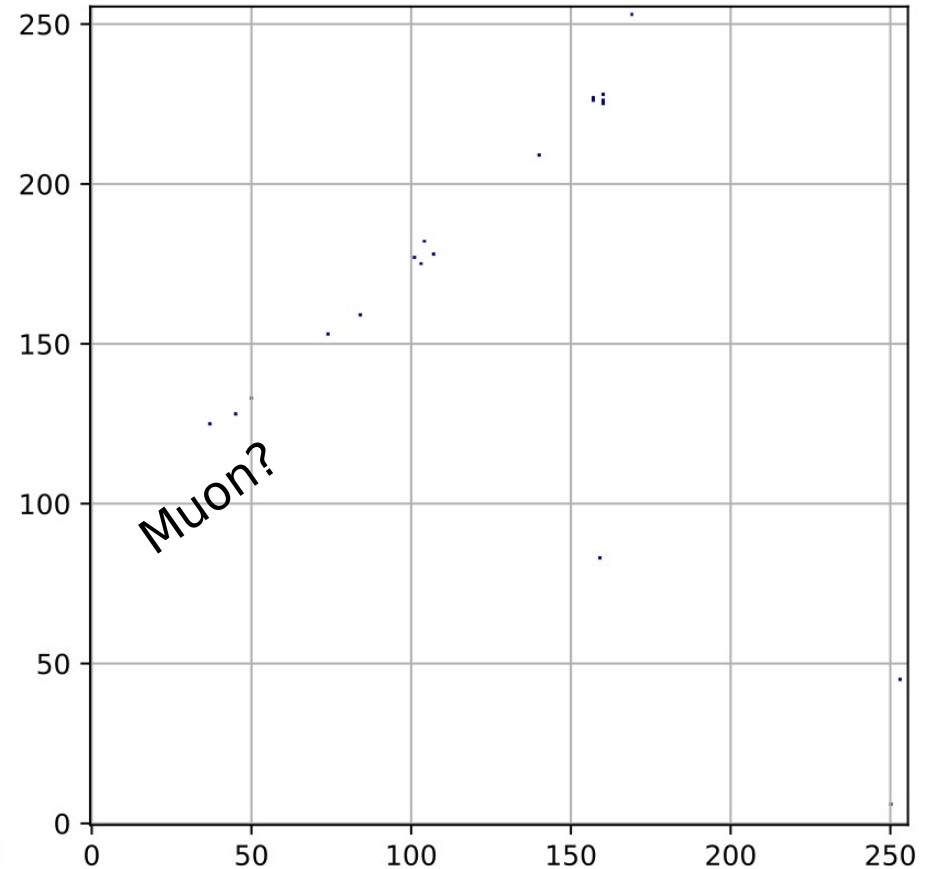
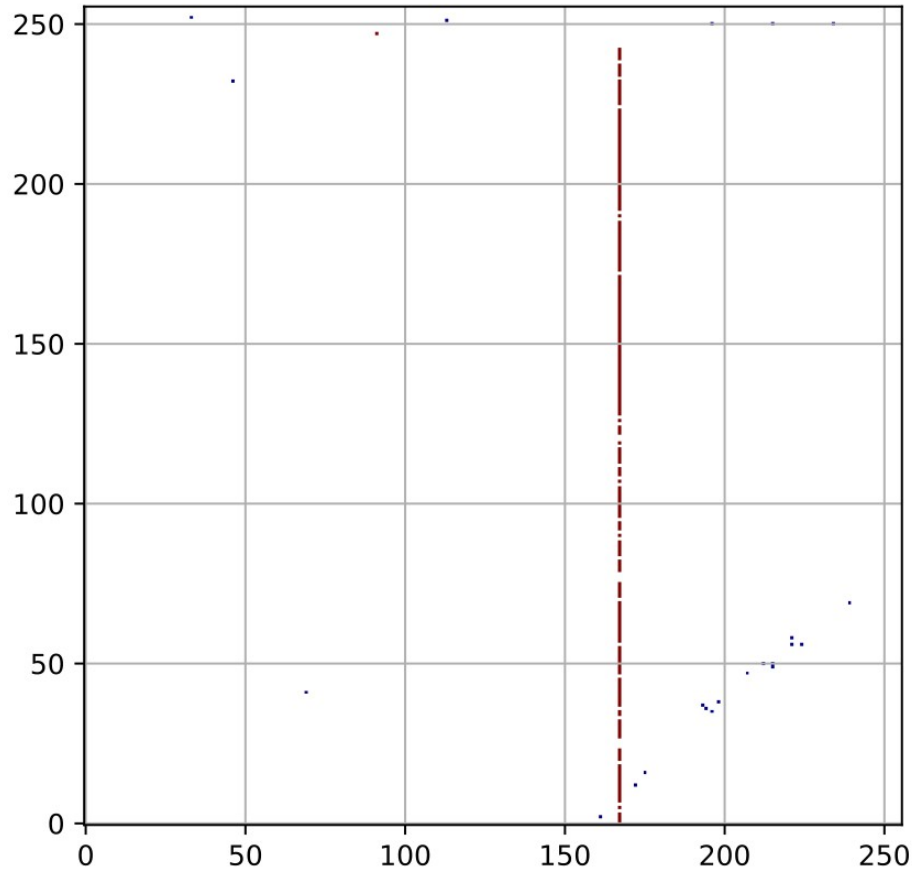


Untriggered events - signatures

Strongly ionizing particles



Untriggered events - signatures



Next steps



Mounting trigger system + Boron converter inside the detector

Beam time! Proof of principle, characterization

Backup



Backup - Name Clarification



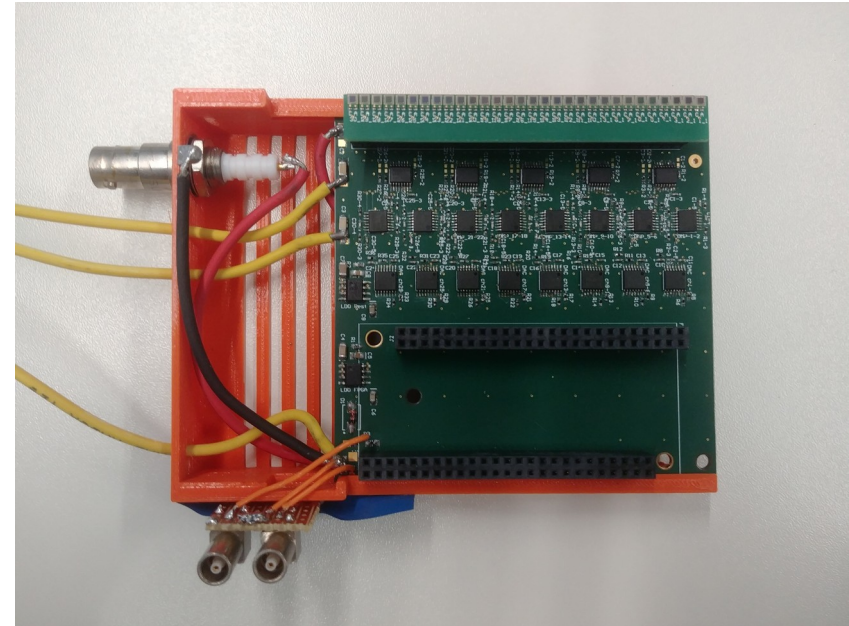
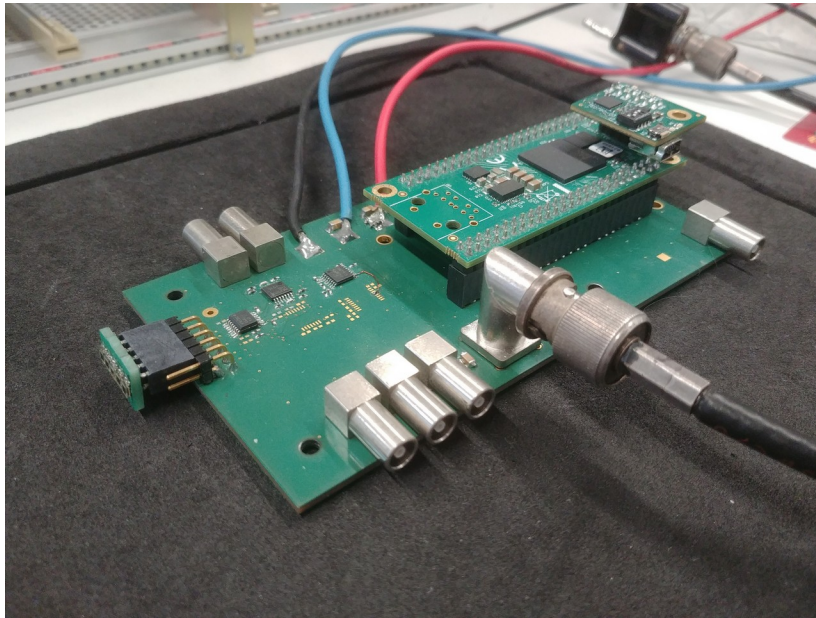
BODELAIRE stands for:

Boron **D**etector with **L**ight and **I**onization **R**econstruction

Backup - Trigger boards

4 channels

30 channels



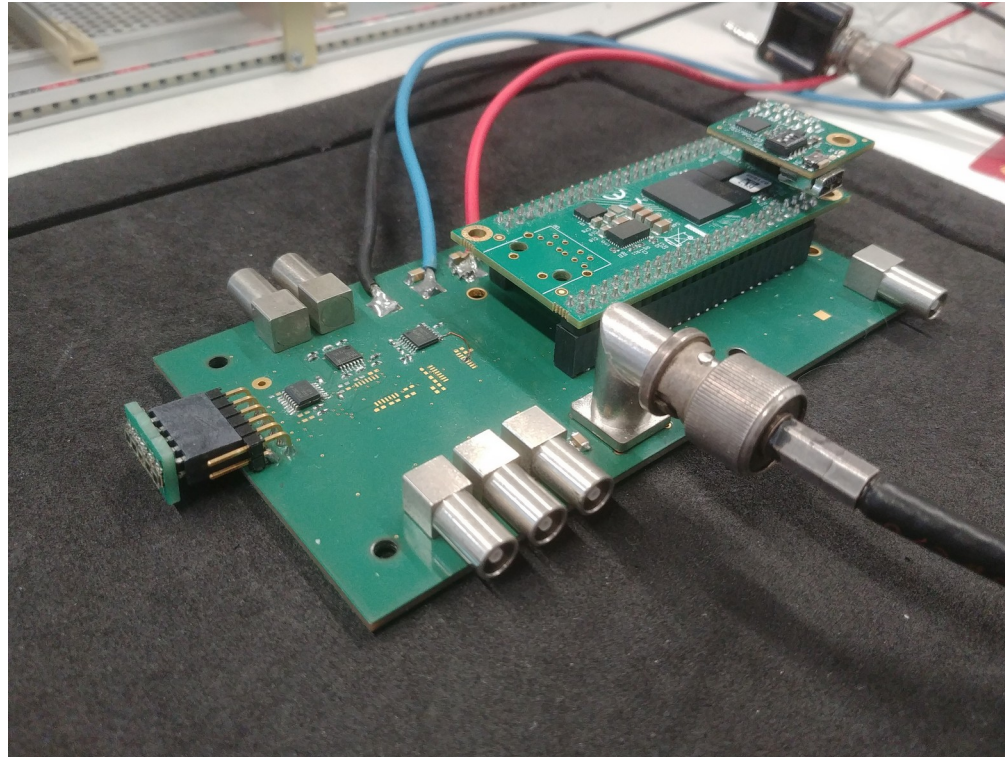
Backup - 4ch Trigger board

Board powering

Artix-7
FPGA Board

Amplifier output

SiPMs



USB to PC

Trigger out

SiPM bias voltage

Backup - Trigger board control

Python script

```
tpc@tpc09: ~/nTPC/Trigger/FPGA codes/SIPM_...
Menu
[ ] - Initialize board
[ ] - Start trigger
[ ] - Stop trigger
[ ] - Set DAC register values
[ ] - Start a threshold scan
[ ] - Quit the application

tpc@tpc09: ~/nTPC/Trigger/FPGA codes/SIPM_...
Here you can set values for different DAC registers

Channel  Value  New
=         1    25  [  ]
T         2    28  [124]
h         3    22  [  ]
r         4    85  [  ]
=
=====
=         1    13  [  ]
O         2    32  [  ]
f         3    43  [  ]
f         4    20  [  ]
=

<< 1/1 >>

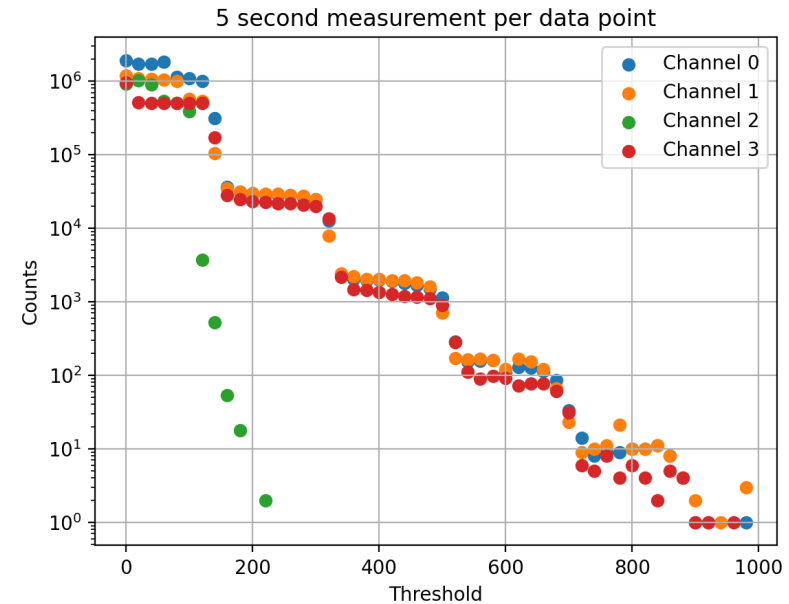
[ ] save to file
[ ] load from file
[ ] exit

tpc@tpc09: ~/nTPC/Trigger/FPGA codes/SIPM_...
Start a threshold scan

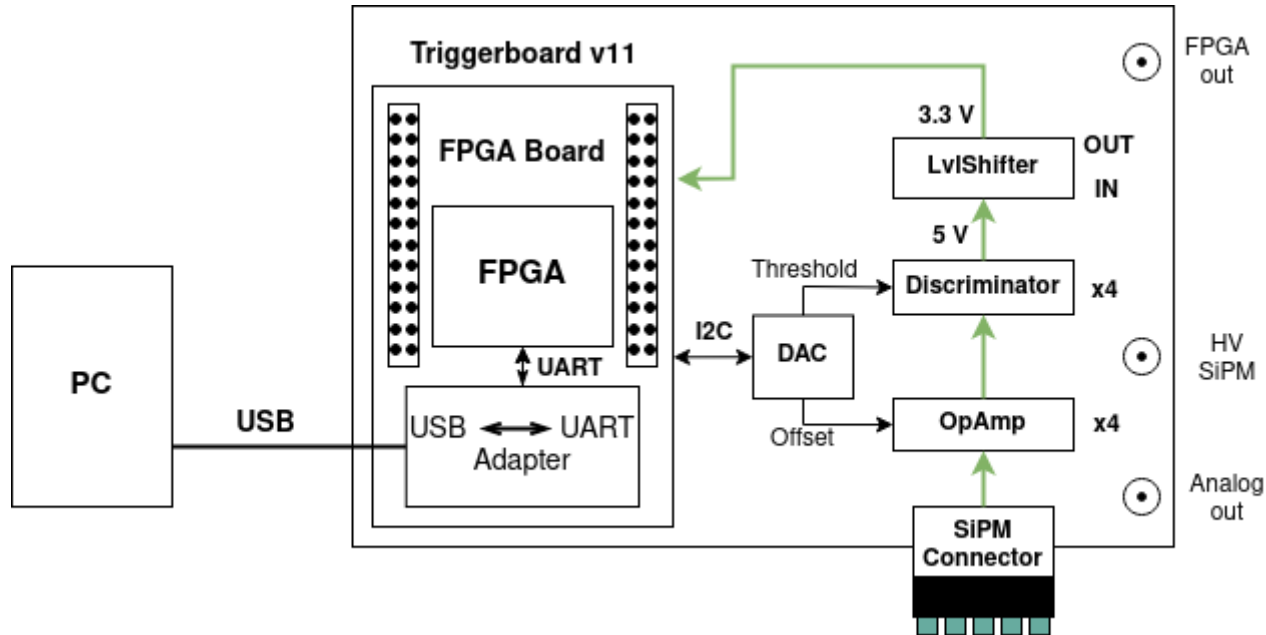
Start  0  [  ]
Stop  4095 [  ]
Step  1  [  ]

[ ] start
[ ] exit
```

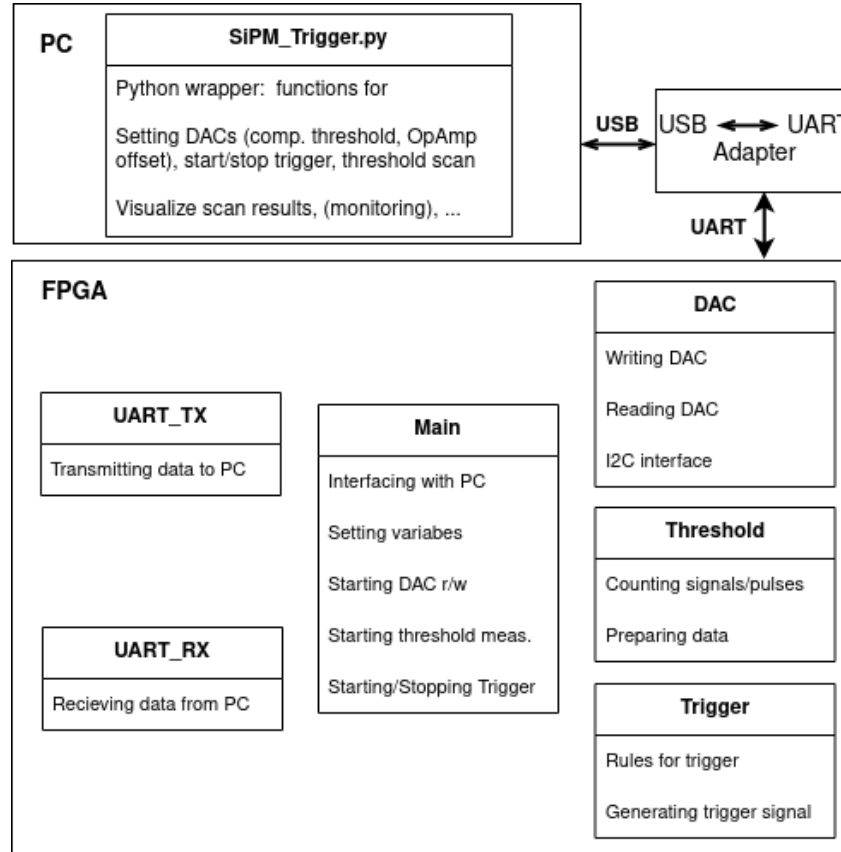
Threshold scan with dark counts



Backup - Trigger board schematic

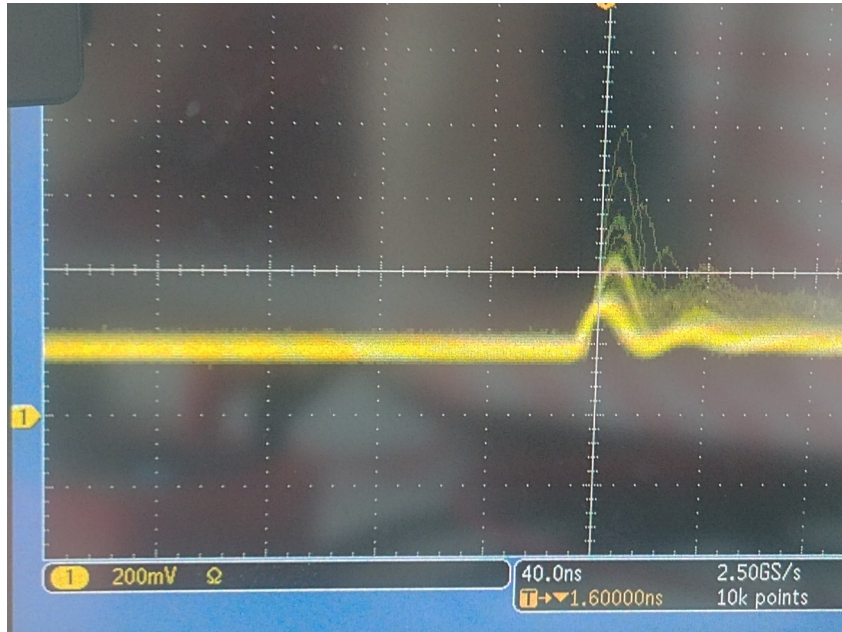


Backup - FPGA firmware + Interface



Backup - SiPM amplifier output

cross talk and afterpulses



threshold scan with dark counts

